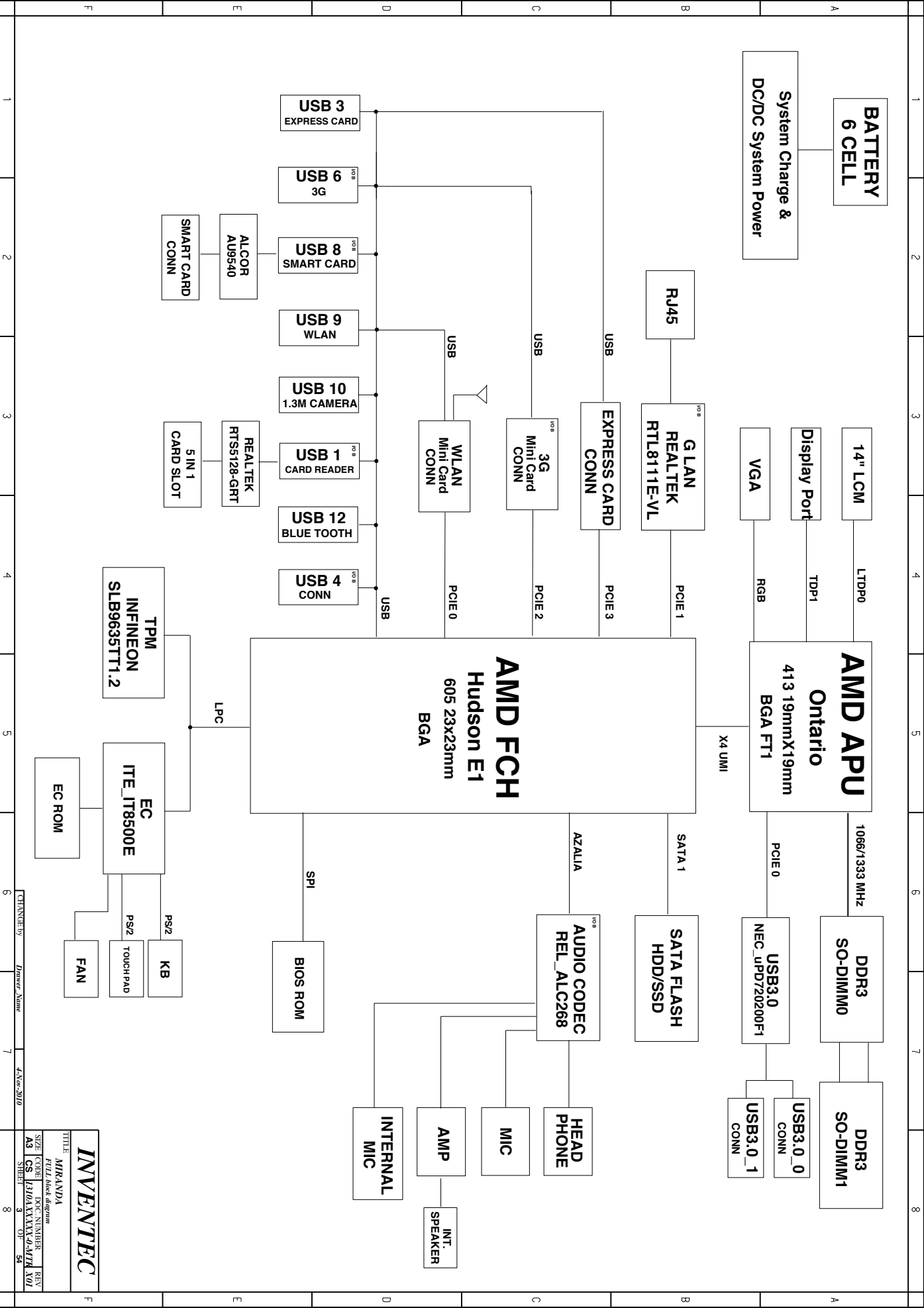


2010.11.05

[illegible]

<i>INVENTEC</i>			
TITLE		MIRANDA	
COVER PAGE			
SIZE CODE	DOC NUMBER	REV	
A3	CS	131014XXXX-0-MIR X01	
SHEET	1		OF 54
DATE	POWER	DATE	
DRAWER			
DESIGN			
CHECK			
RESPONSIBLE			
SIZE = 3			
FILE NAME	XXXX XXXXXXXX	VER.	
PIN	XXXXXXXXXXXX		

	1	2	3	4	5	6	7	8	
A	<div>TABLE OF CONTENTS</div>								
B	PAGE				PAGE				
C	1. COVER PAGE					I/O BOARD			B
	2. INDEX					40. CODEC REL_ALC268			
	3. BLOCK DIAGRAM					41. AUDIO JACK			
	4. POWER BLOCK DAIGRAM					42. AMP / INT. SPEAKER			
	5. POWER ON SEQUENCE					43. WWAN			
	6. POWER ON SEQUENCE					44. SMART CARD AU9540			
	7. POWER CHARGE/DC IN					45. CARD READER			
	8. POWER BATTERY					46. GIGA LAN			
	9. POWER +V3LA/+V3A/+V5A					47. RJ45			
	10. POWER +V1.8S/+V0.75S/+V1.5					48. USB CONN			
	11. POWER +V1.1A/+V1A/+V1.1S					49. WIRE to BOARD CONN (I/O/B SIDE)			D
	12. POWER VCORE/CORE_NB					TP BOARD			
	13. POWER +V5S/+V3S/+V1.1S					50. TP / HALL-SENSOR			
	14. APU FT1 1					POWER BOARD			
	15. APU FT1 2					51. POWER SW			E
	16. APU FT1 3					52. EMI			
	17. APU FT1 4								
	18. DDR3-1								
	19. DDR3-2								
F	1	2	3	4	5	6	7	8	F
<div>CHANGED BY</div> <div>Drawn Name</div> <div>11-05-2010</div>						<div><div>INVENTEC</div><div><div>TITLE</div><div>MIRANDA</div><div>index</div></div><div><div>SIZE</div><div>A3</div></div><div><div>CODE</div><div>CS</div></div><div><div>DOC. NUMBER</div><div>1310AXXX3-2-MIRK30</div></div><div><div>REV</div><div>1</div></div><div><div>SHEET</div><div>2</div></div><div><div>OF</div><div>54</div></div></div>			

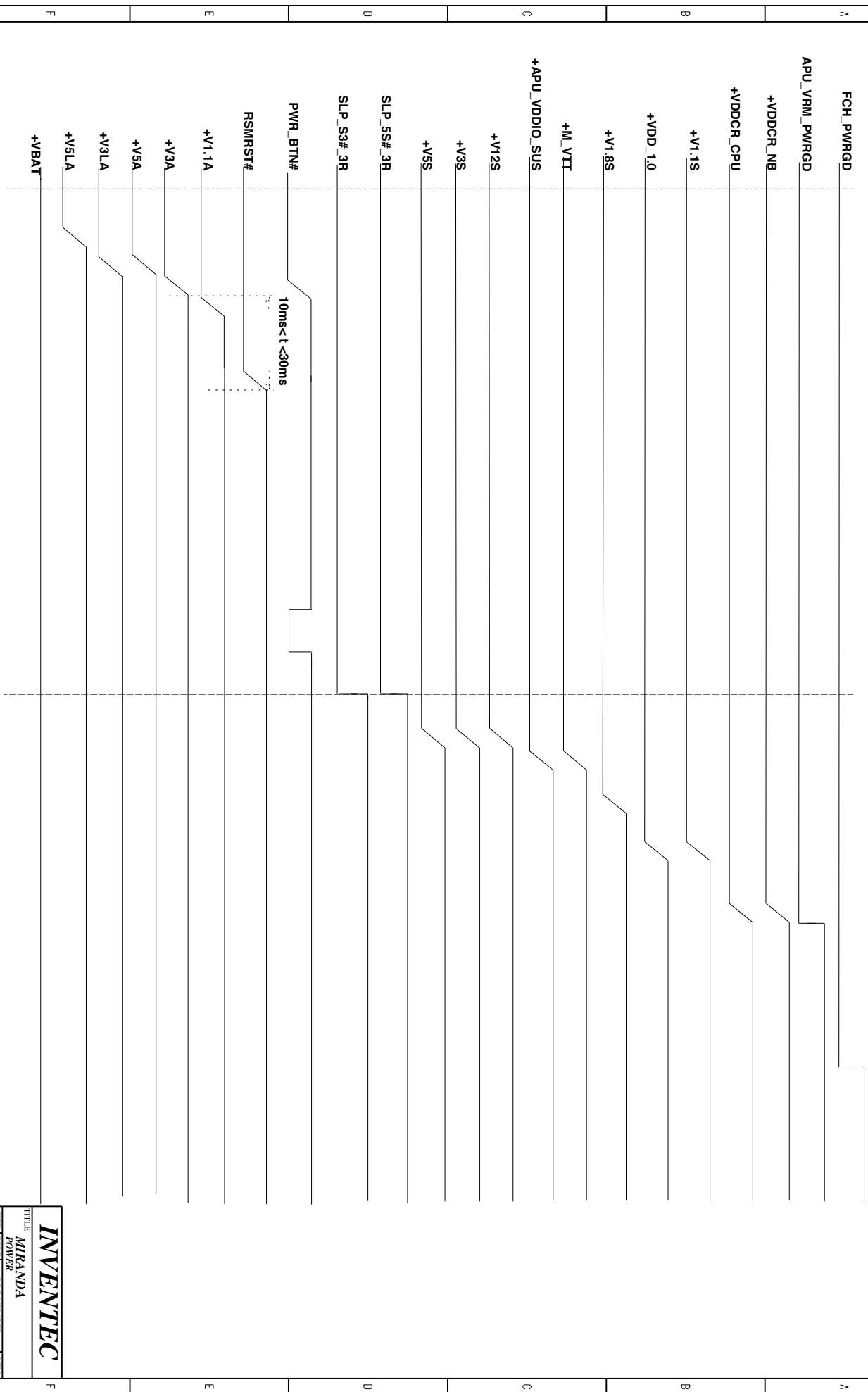


Power Block Diagram

The diagram illustrates the power distribution architecture for the MIRANDA system. It shows the flow of power from the ADAPTER through various regulators and switches to the system components. Key components include the VCCORE VNB (ISL6267HRTZ-T), TPS51218DSCR (1.5V, 1V), AONZ410, AO6402AL, GS173R41U, and G2997F6U. The diagram is labeled 'Power Block Diagram' and includes a title block for INVENTEC MIRANDA POWER ON SEQUENCE.

ITEM	DESCRIPTION	QTY	UNIT
1	ADAPTER	1	PCB
2	VCCORE VNB (ISL6267HRTZ-T)	1	IC
3	TPS51218DSCR (1.5V)	1	IC
4	TPS51218DSCR (1V)	1	IC
5	AONZ410	2	IC
6	AO6402AL	2	IC
7	GS173R41U	4	IC
8	G2997F6U	1	IC

POWER ON SEQ.



1	2	3	4	5	6	7	8
1	2	3	4	5	6	7	8

INVENTEC

MIRANDA

POWER

SIZE

CODE

AS

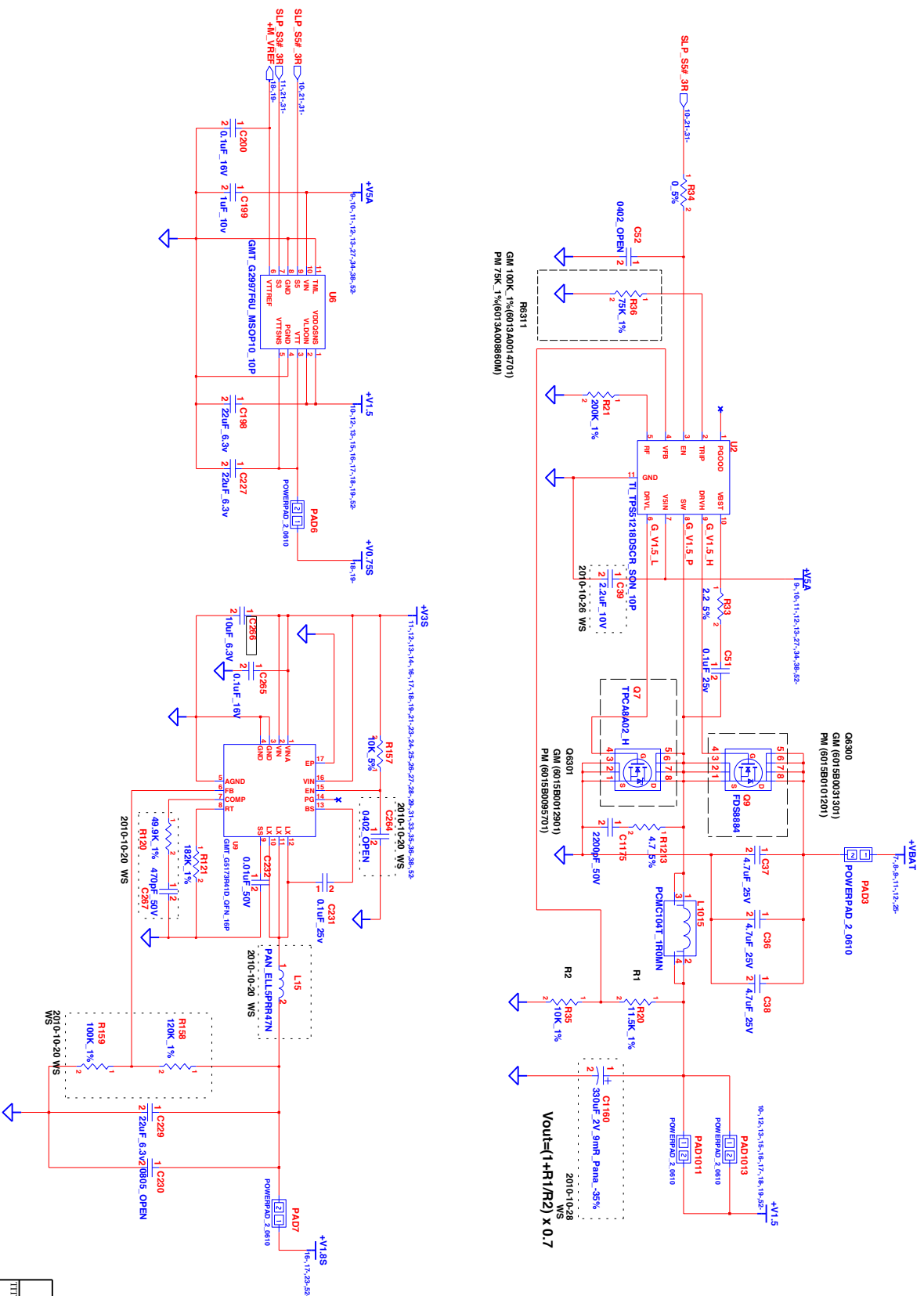
DOC:NUMBER

REV

5 - OF - 54

[illegible]

+V1.8S +V0.75S +V1.5



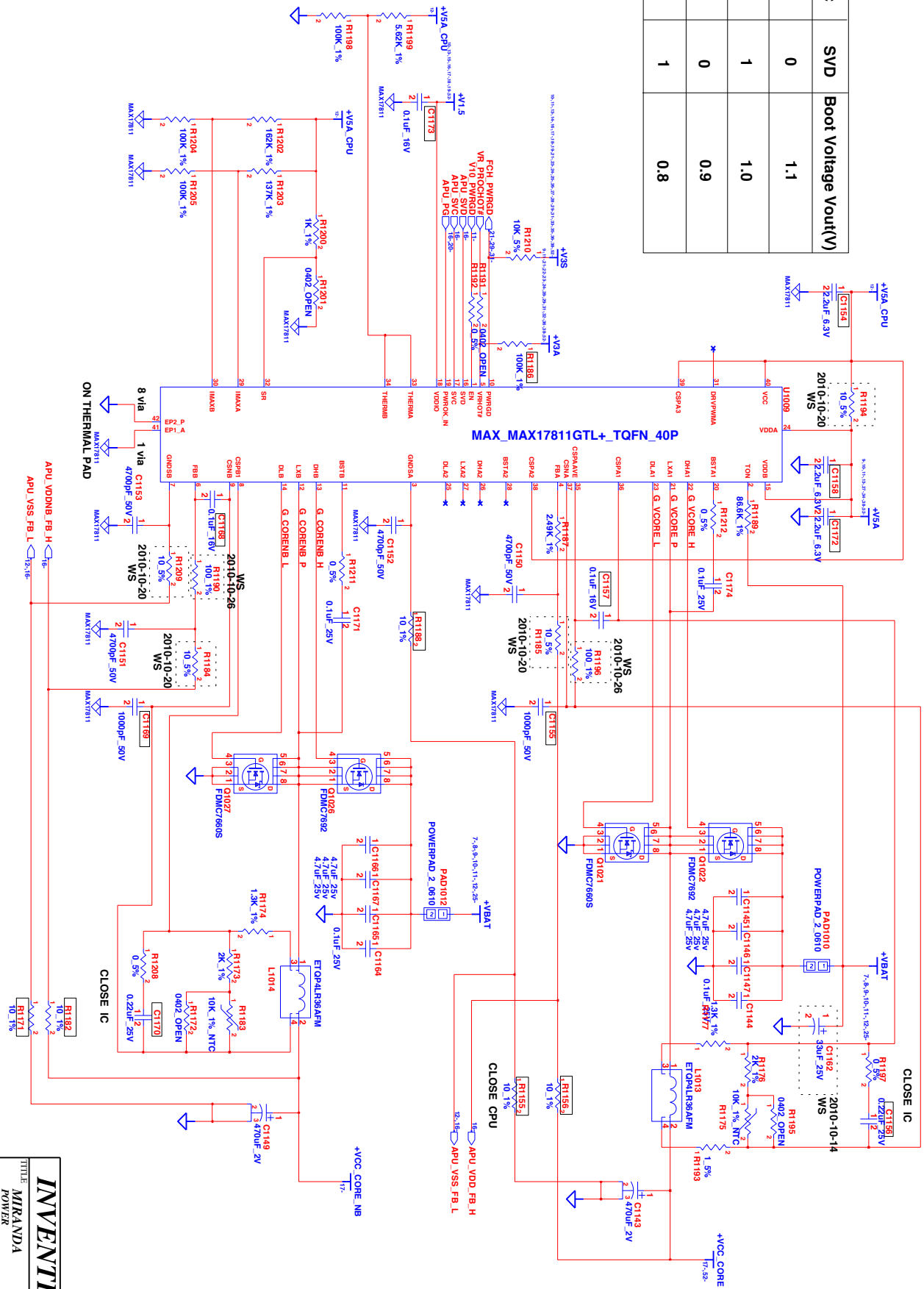
INVENTEC
MIRANDA
POWER

SIZE CODE
A3 CS1

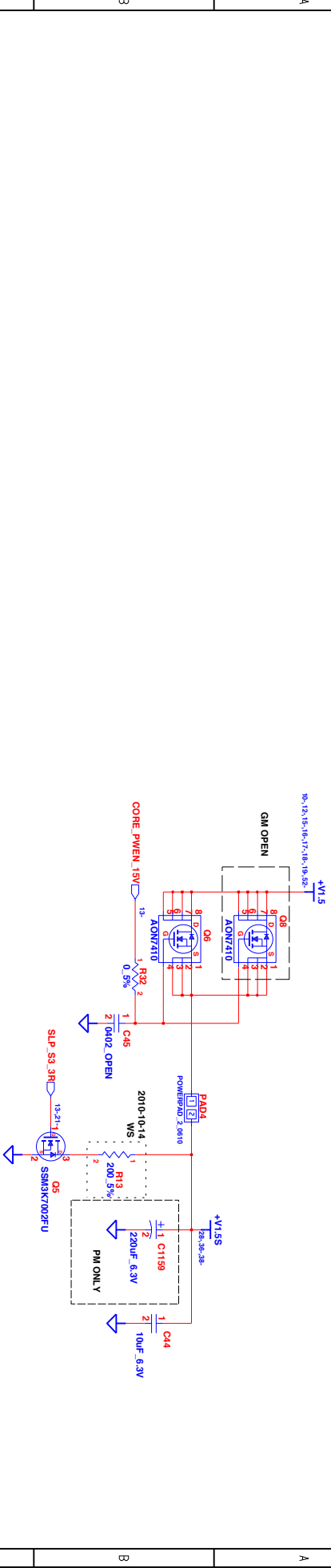
DOC NUMBER
10 OF 54

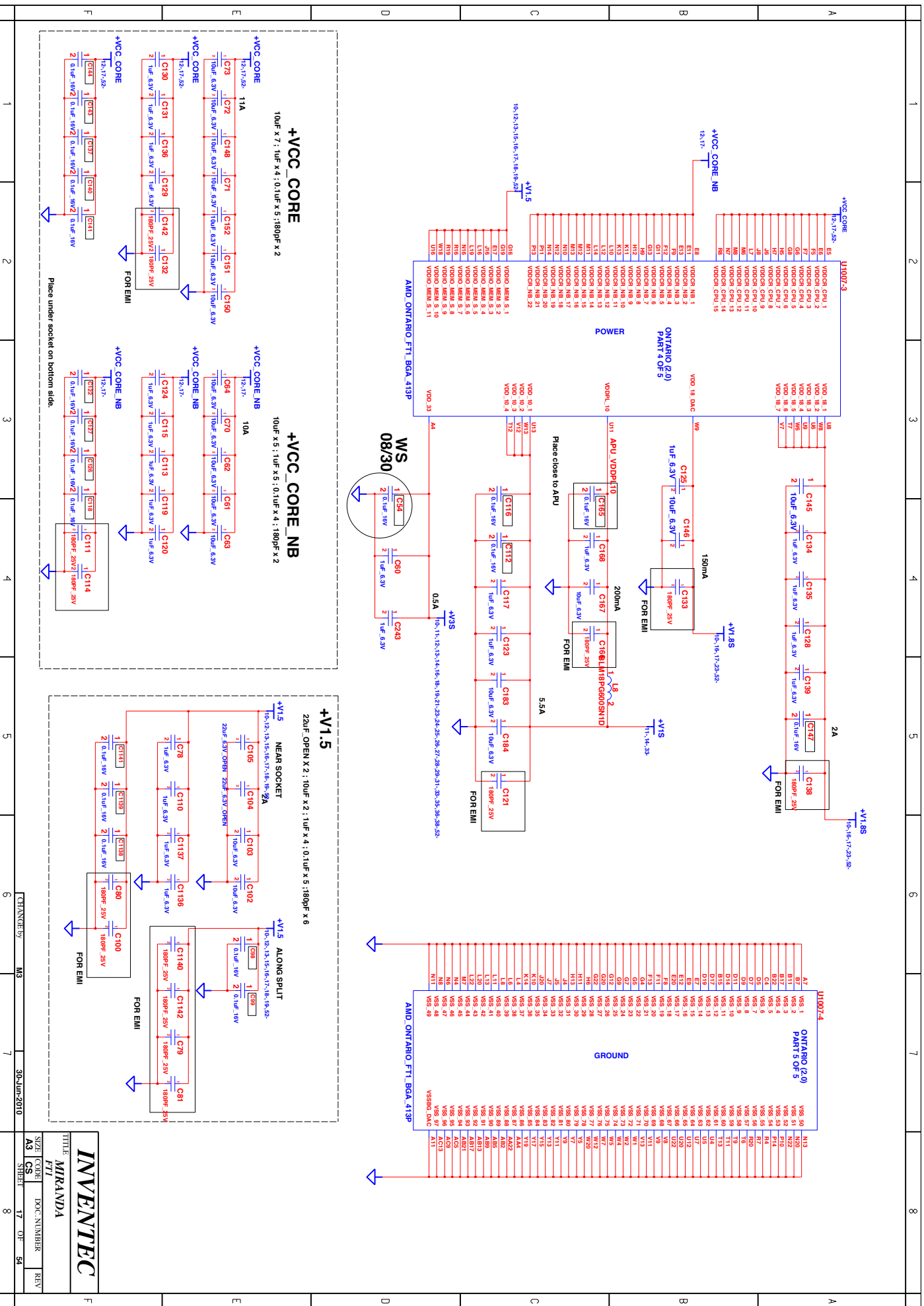
[illegible]

SVC	SVD	Boot Voltage Vout(V)
0	0	1.1
0	1	1.0
1	0	0.9
1	1	0.8



<i>INVENTEC</i>			
TITLE			
MIRANDA			
POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS		
SHEET	12	OF	54

[illegible][illegible]



INVENTEC
MIRANDA
FT1

SIZE CODE
A3 CS

DOC NUMBER
17 OF 54

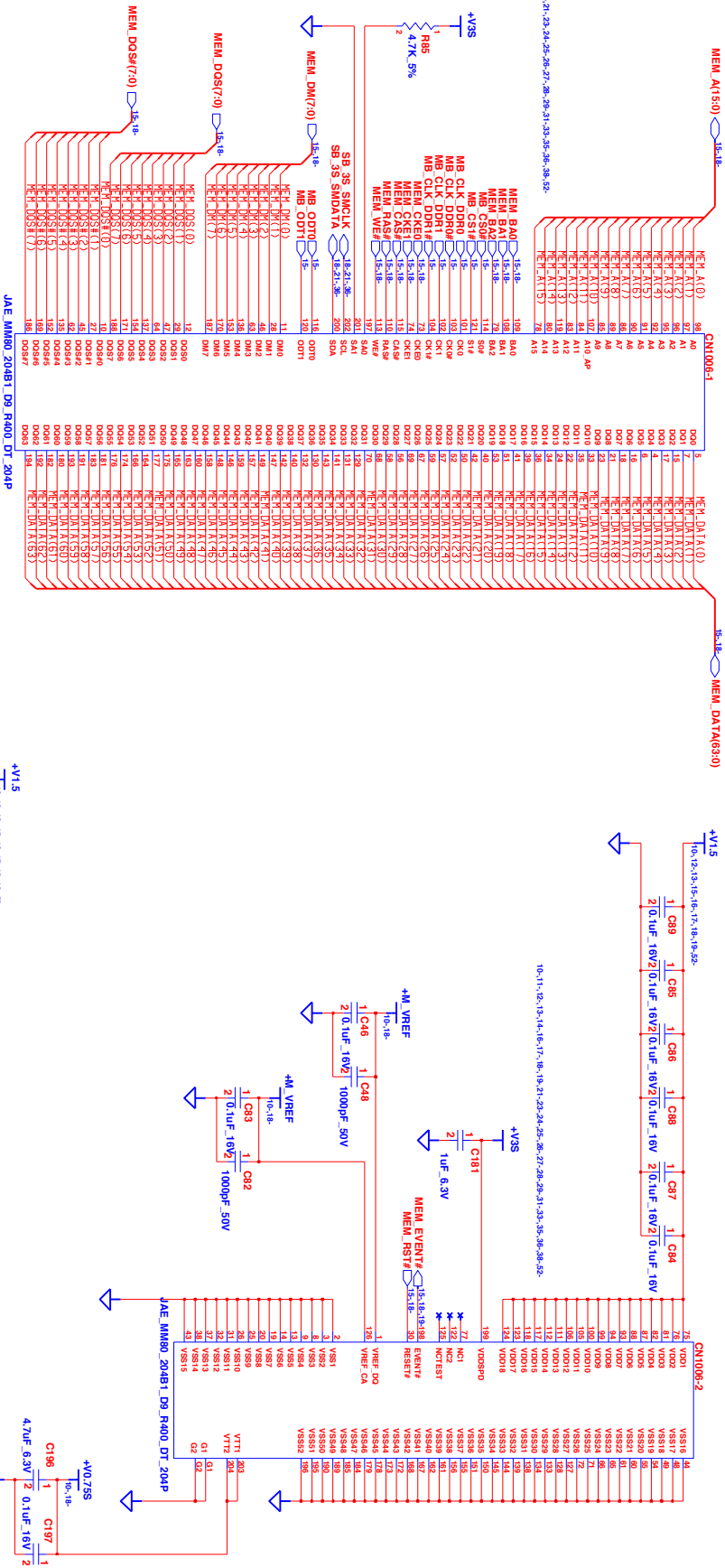
REV

www.vinafix.vn



88

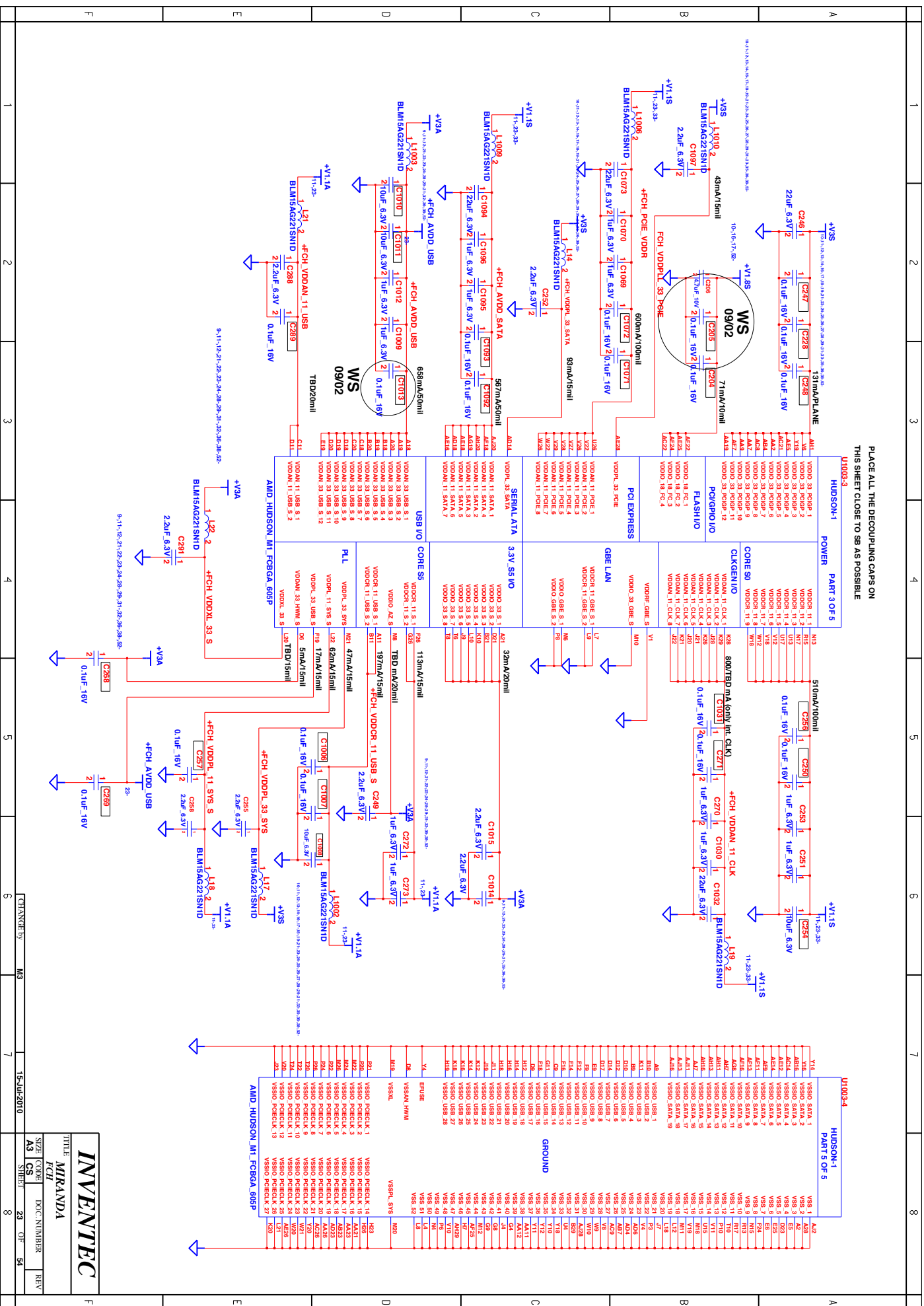
DDR3



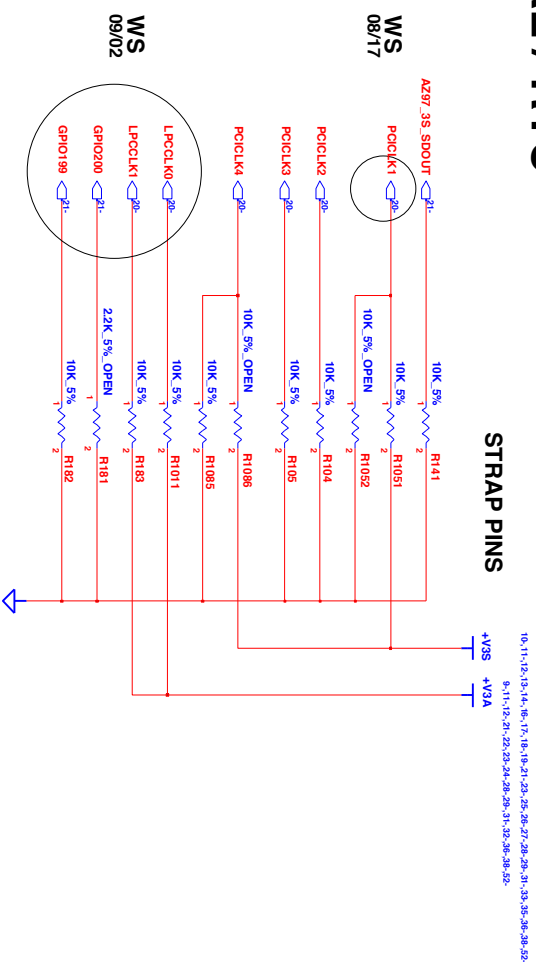
SO DIMM1

SLOT B(STANDARD)

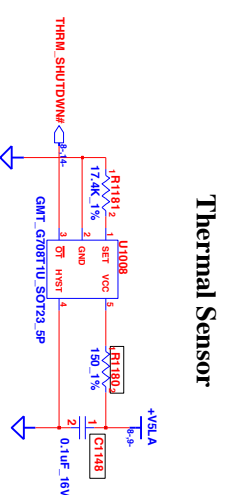
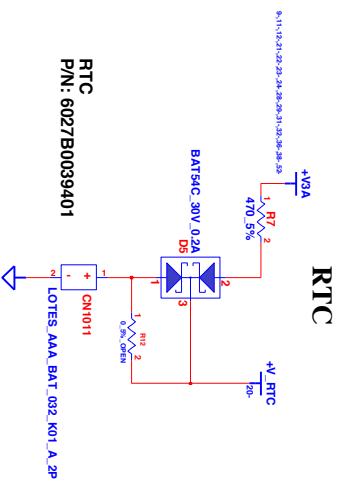
INVENTEC		F	
MIRANDA		E	
DDR3-2		D	
A3 CS		C	
SIZE CODE		B	
19		A	
8		1	
REV		1	
DCC NUMBER		2	
STREET		3	
28-Aug-2010		4	
MIRANDA		5	
CITANCHO		6	
7		7	
8		8	



STRAP OPTION / THERMAL / RTC



	AZ97_SS_SPDOUT	PCI CLK1	PCI CLK2	PCI CLK3	PCI CLK4	LPCCCLK0	LPCCCLK1	GPIO200 EC_PWM3	GPIO199 EC_PWM2
PULL HIGH	Low Power Mode (Not Supported)	Allow PCIe Gen2 (Default)	Watchdog Timer Enabled (Not Supported)	Debug STRAP Enabled	Non-FUSION CLOCK Mode	Integrated Micro-Controller Enabled (Default)	Internal Clock Generator Enabled (Default)		
PULL LOW	Performance Mode (Default)	Force PCIe Gen1	Watchdog Timer Disabled (Default)	Debug STRAP Disabled (Default)	FUSION CLOCK Mode (Default)	Integrated Micro-Controller Disabled	Internal Clock Generator Disabled		



CHANGE by MIRANDA

MIRANDA

2-Sep-2010

24 OF 5

INVENTEC

TITLE
MIRANDA

STRAP OPTION / THERMAL / PVC

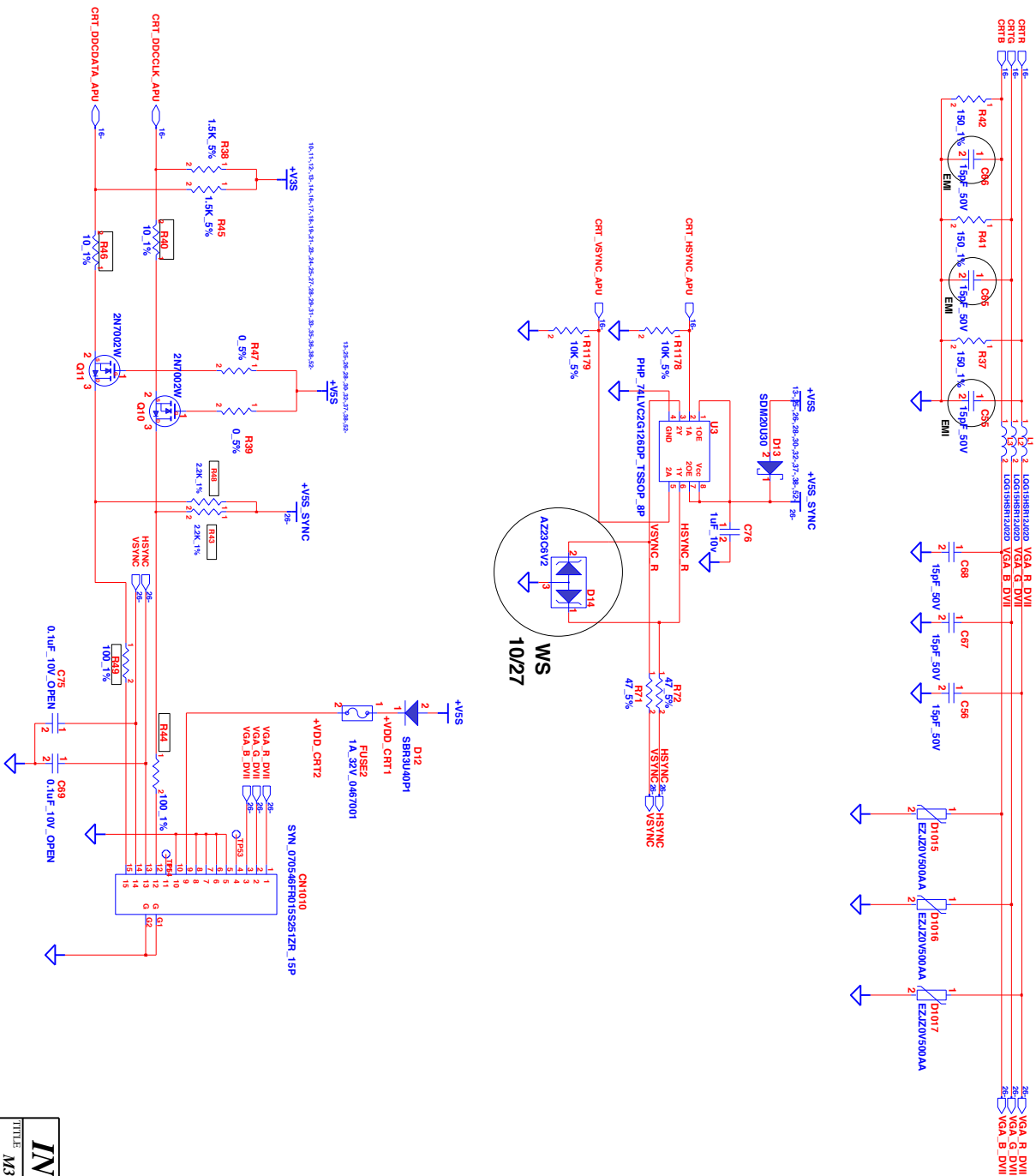
SIZE	CODE	DOC. NUMBER	REV
A3	CS		

PAGE 24 OF 50



<i>INVENTEC</i>			
TITLE MIRANDA			
DVI-I			
SIZE	CODE	DOC. NUMBER	REV.
A3	CS		
SHEET		25	OF 54

CRT

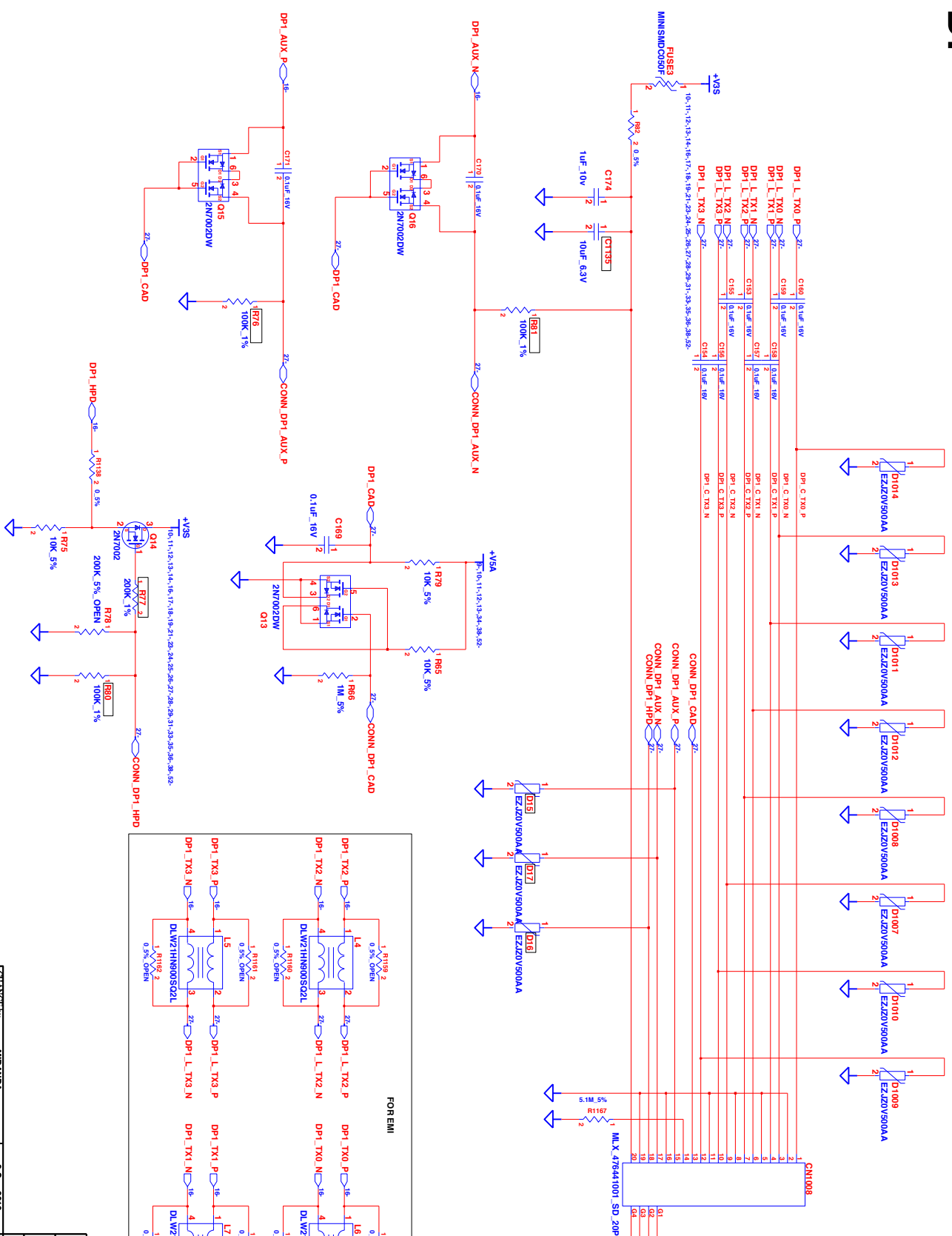


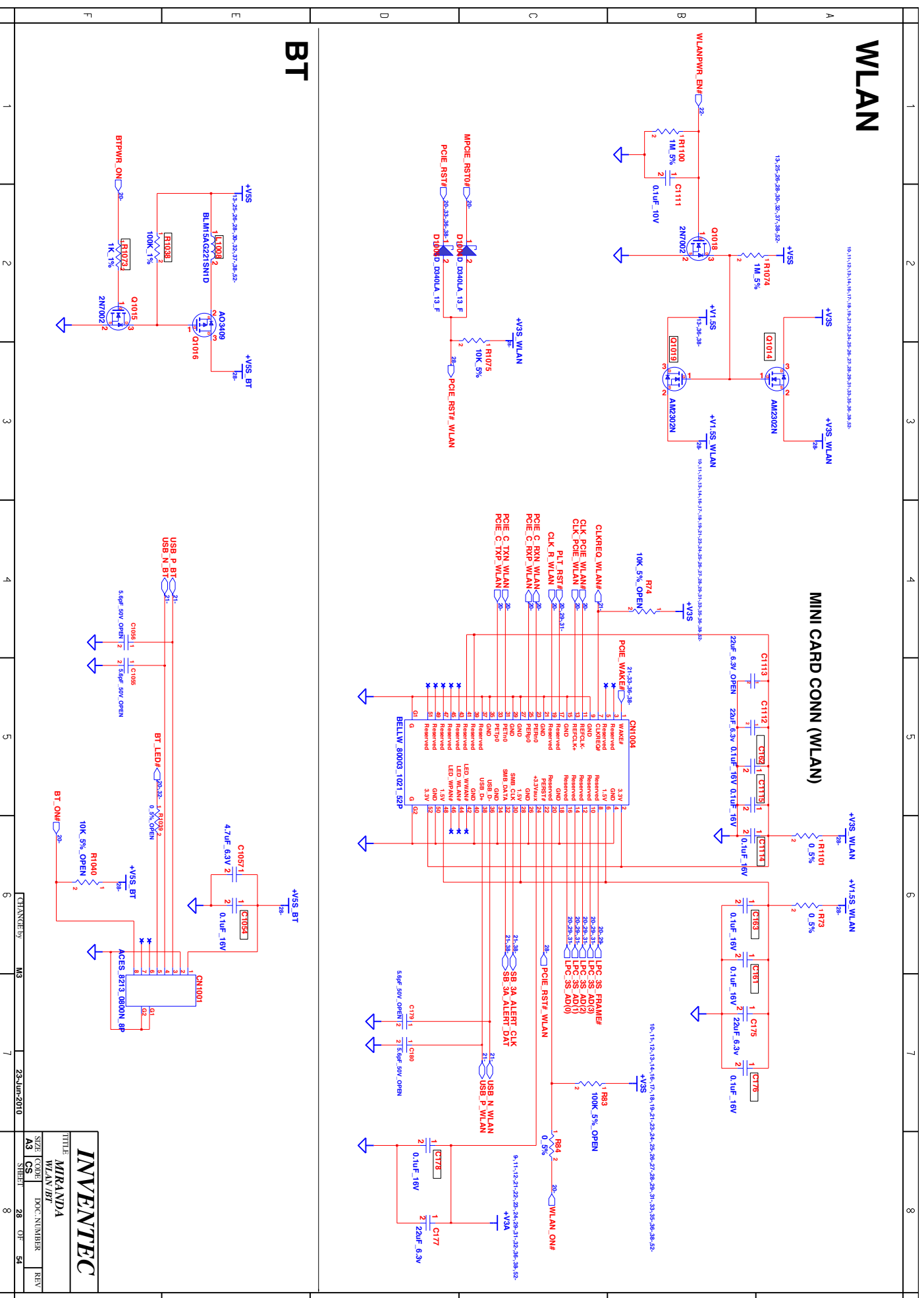
INVENTEC

M3

SIZE CODE
A3 CS

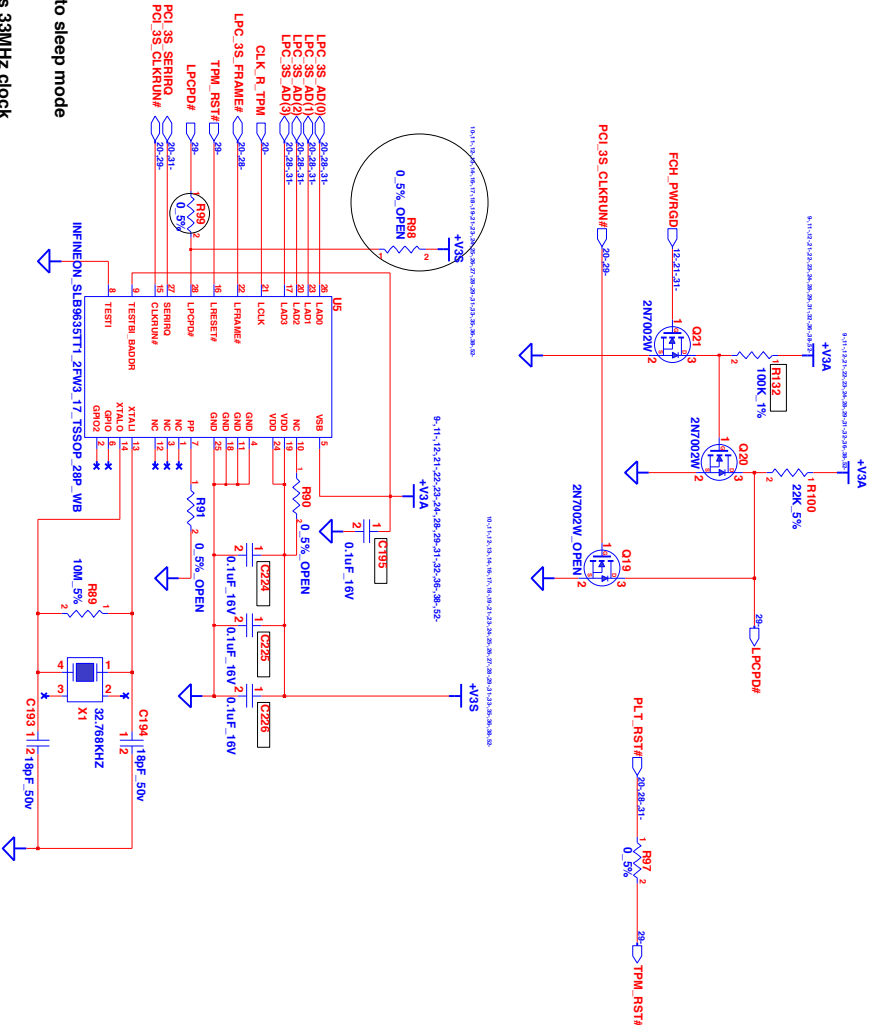
DOC. NUMBER
26 OF 54





TPM

LPC_PWRDWN_L is used to force LPC peripherals to go into sleep mode
Device may require that this be asserted before entering S3
Device may require that this be asserted before stopping this 33MHz clock



INVENTEC

TITLE
MIRANDA

SIZE CODE
A3 CS

DOC NUMBER
29 OF 54

REV

DATE
2010

2-JUL-2010

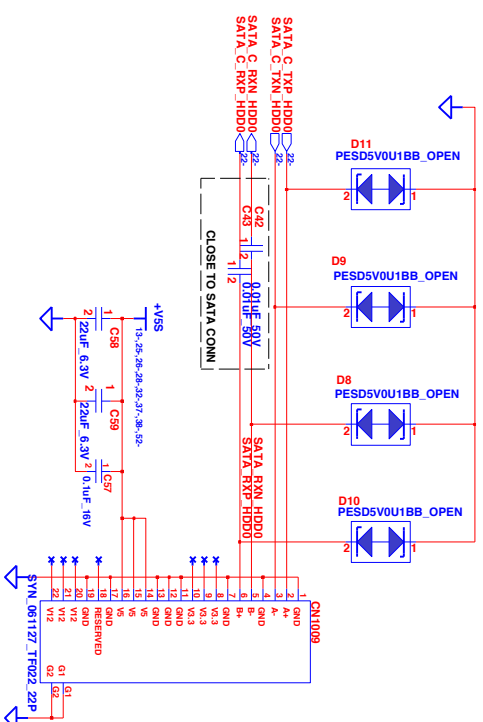
7

8

8

8

SSD / HDD



CHANGED by

MIRANDA

6

7

2-Sep-2010

INVENTEC

TITLE

MIRANDA

SATA FLASH / SSD / HDD

SIZE

CS

30 OF 54

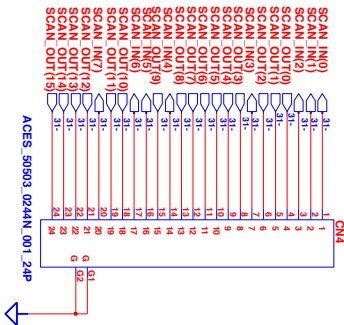
DOC. NUMBER

REV

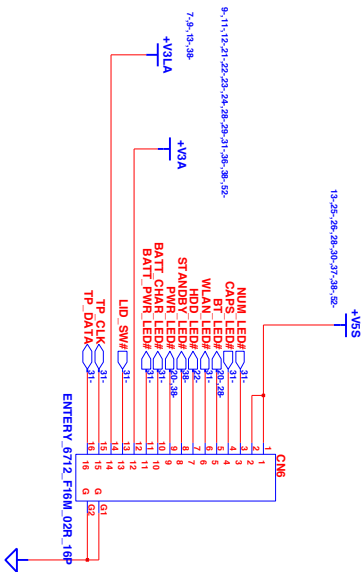
30 OF 54

KB/TP CONN

Keyboard Conn.



TP/B Conn. M/B SIDE



INVENTEC

MIRANDA

NO

SIZE

CODE

CS

SHR

32

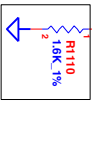
OF

54

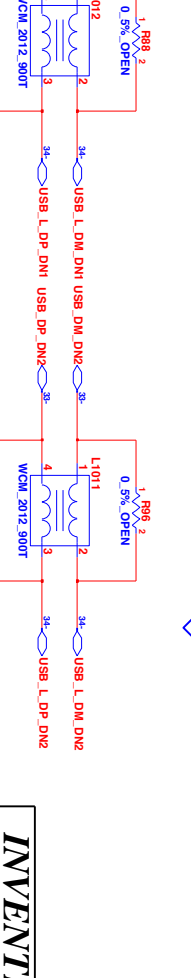
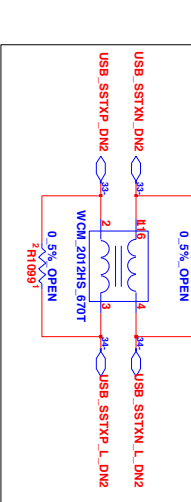
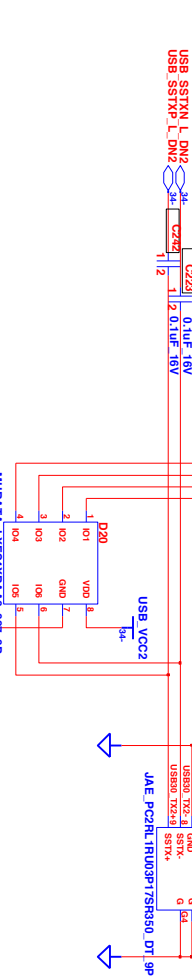
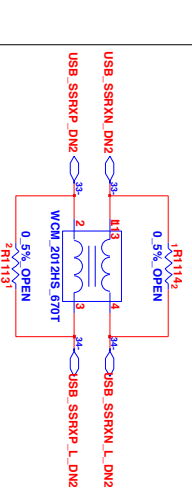
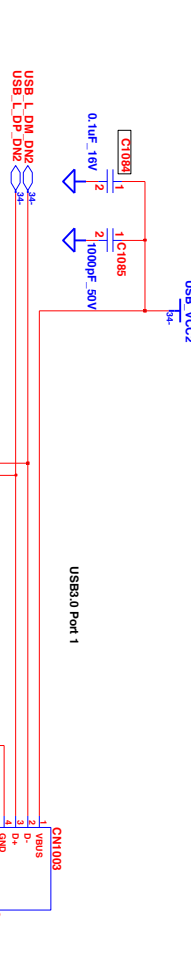
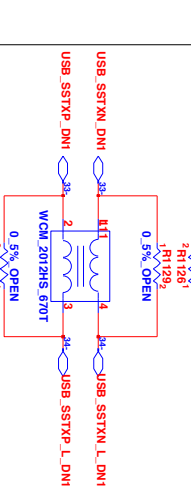
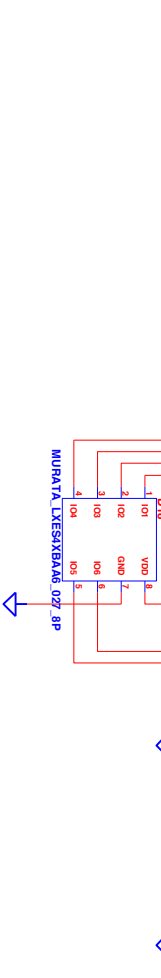
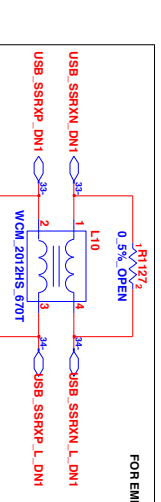
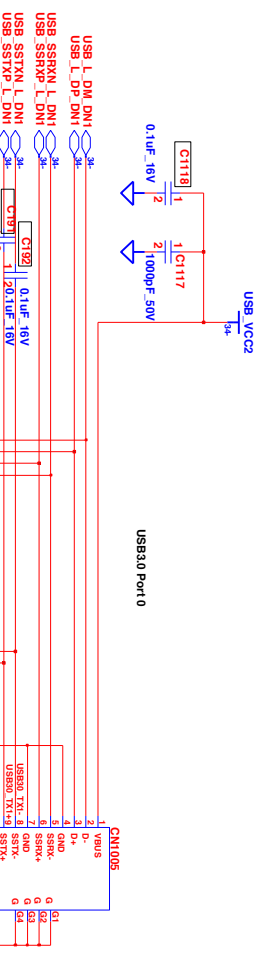
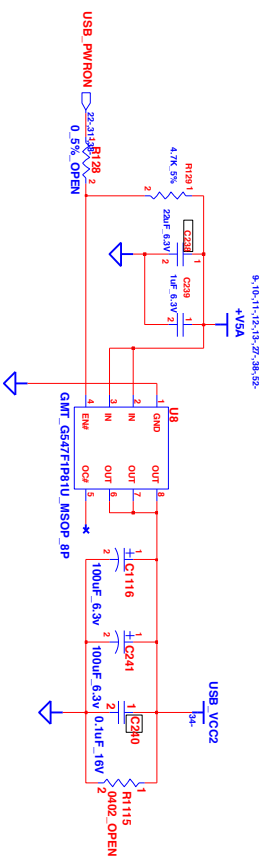
REV

DOC NUMBER

$\text{C}^0\text{-CC}^1\text{-CC}^0\text{-CC}^1$, $\text{C}^0\text{-CZ}^1\text{-CZ}^1\text{-CZ}^1$, $\text{C}^0\text{-CZ}^1\text{-CZ}^1\text{-CZ}^1$, $\text{C}^0\text{-CZ}^1\text{-CZ}^1\text{-CZ}^1$



DUAL USB 3.0 CONN



<i>INVENTEC</i>			
TITLE: <i>MIRANDA</i>			
DRAWN: <i>USB3.0 CONN./USB HEADER</i>			
SIZE: <i>A3</i>	CODE: <i>CS</i>	DOC. NUMBER: <i>34</i>	REV: <i>54</i>
SHEET: <i>8</i>			

[illegible]

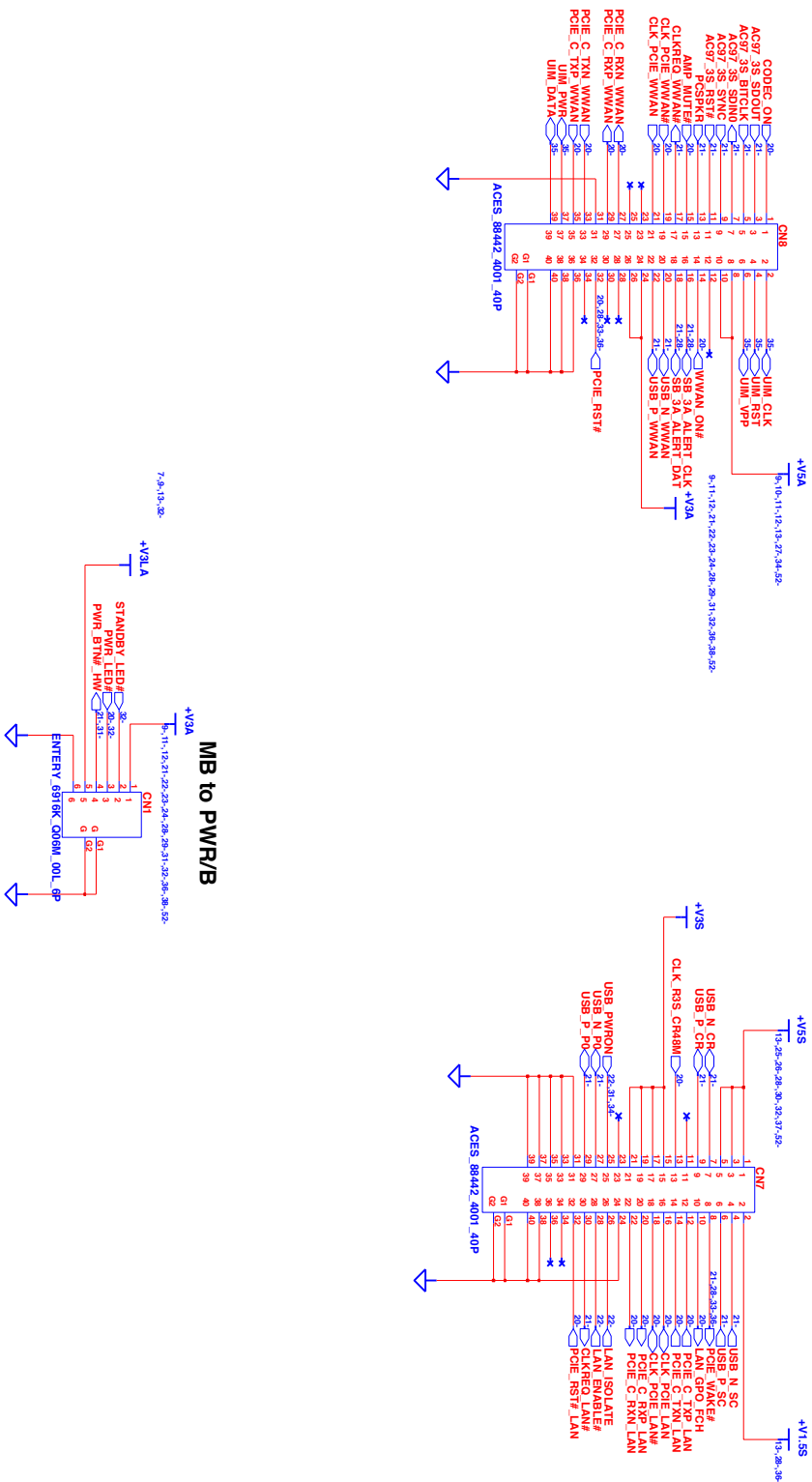
1		2		3		4		5		6		7		8	
SIM CARD															
A															
B															
C															
D															
E															
F															

1		2		3		4		5		6		7		8	
CHANGELY MIRANDA 11-SEP-2010															
INVENTEC															
MIRANDA															
SIZE CODE DOC NUMBER REV															
A3 CS 35 OF 54															
8															

[illegible]

[illegible][illegible]

WIRE to BOARD CONN (MB SIDE)



INVENTEC

MIRANDA

RSER CARD

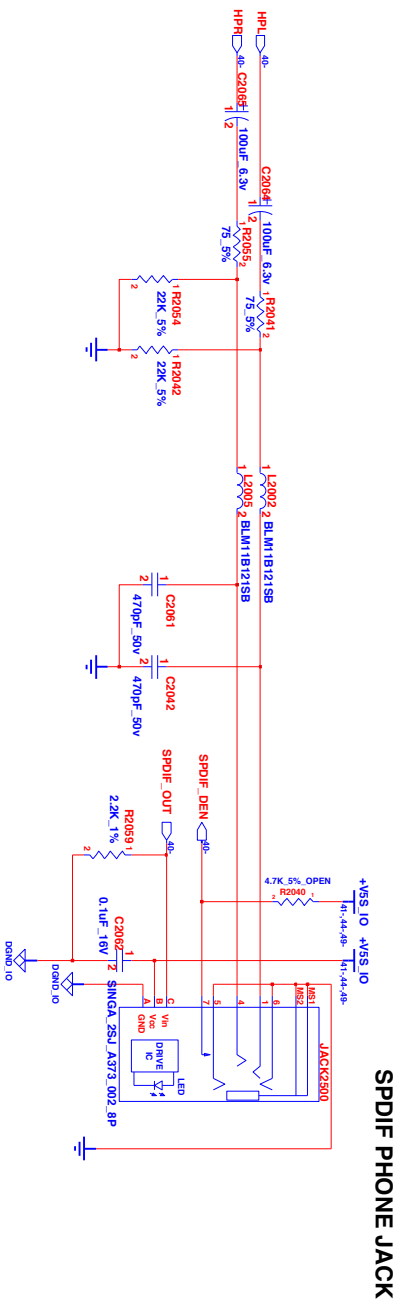
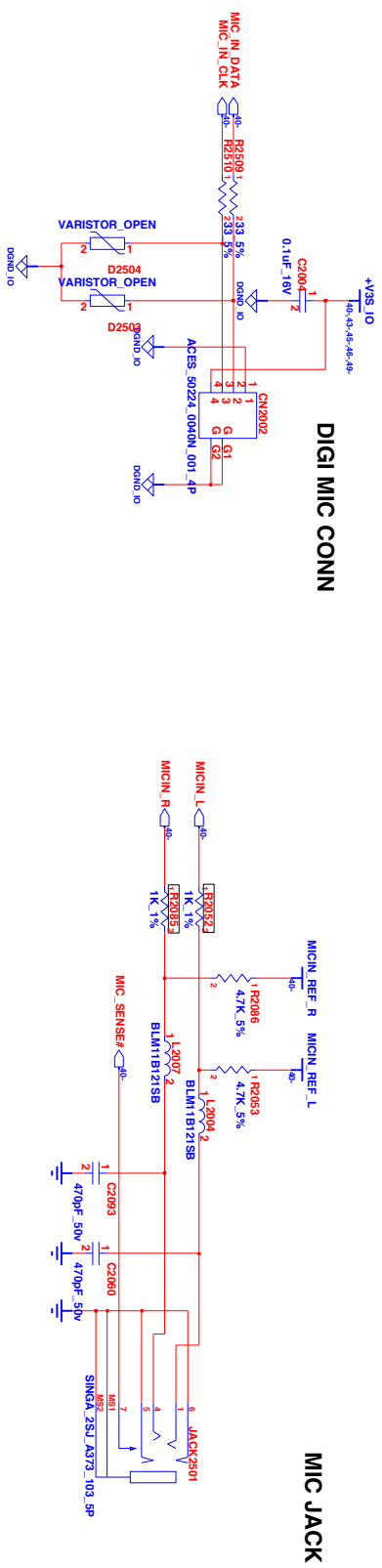
SIZE CODE

AS CS

SHRIT

38 OF 54

AUDIO JACK



INVENTEC

M3

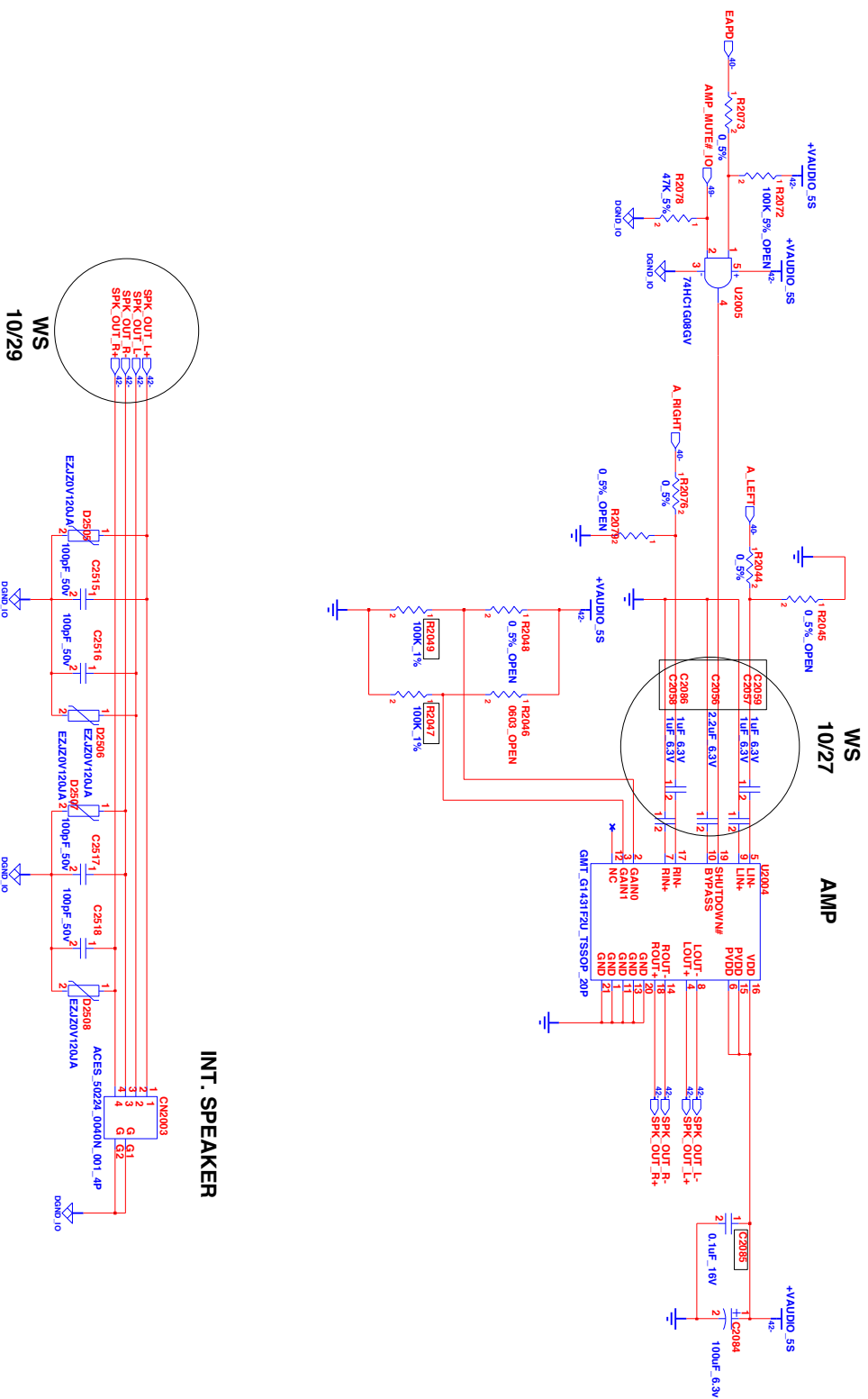
ADDIO JACK

SIZE CODE DOC:NUMBER REV

A3 CS 41 OF 54

AMP / SPEAKER

EAPD when output high, turn on OP
EAPD when output low, turn off OP

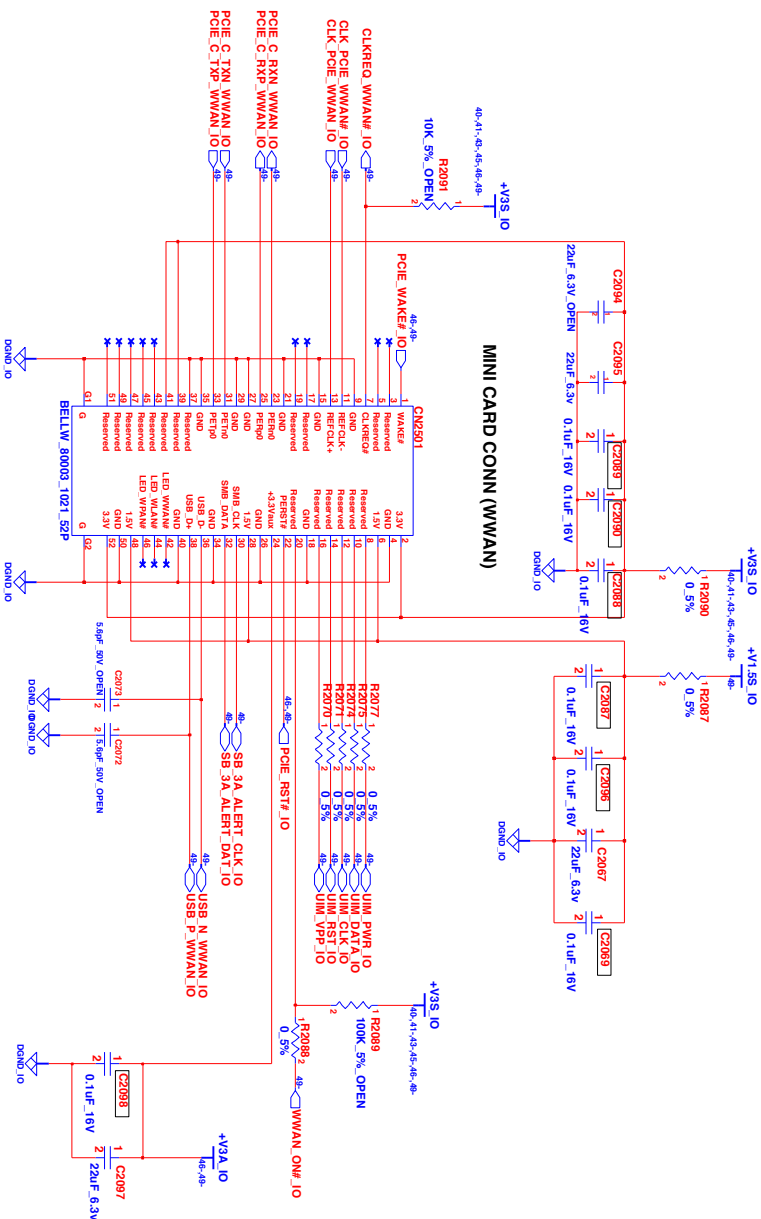


INT. SPEAKER

CHANGE by	M3	23-Jun-2010
-----------	----	-------------

<i>INVENTEC</i>			
TITLE			
MIRANDA			
AMPS/PEAKER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS		
SHEET		42	OF 54

WWAN



INVENTEC

M3

WWAN

SIZE CODE M3
CS
SUBST 43 OF 54

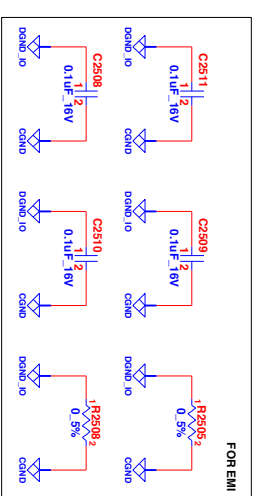
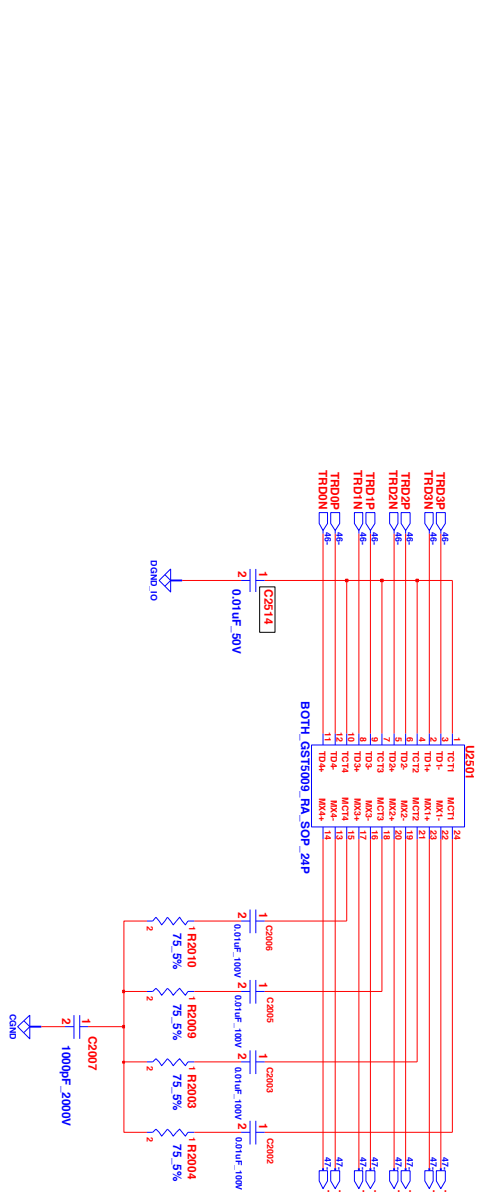
DOC:NUMBER
REV

CHANGED BY: MIFANDA
16-SEP-2010

[illegible]

USB CARD READER

RJ45



INVENTEC

MIRANDA

RJ45 DUAL USB

SIZE CODE

DOC NUMBER

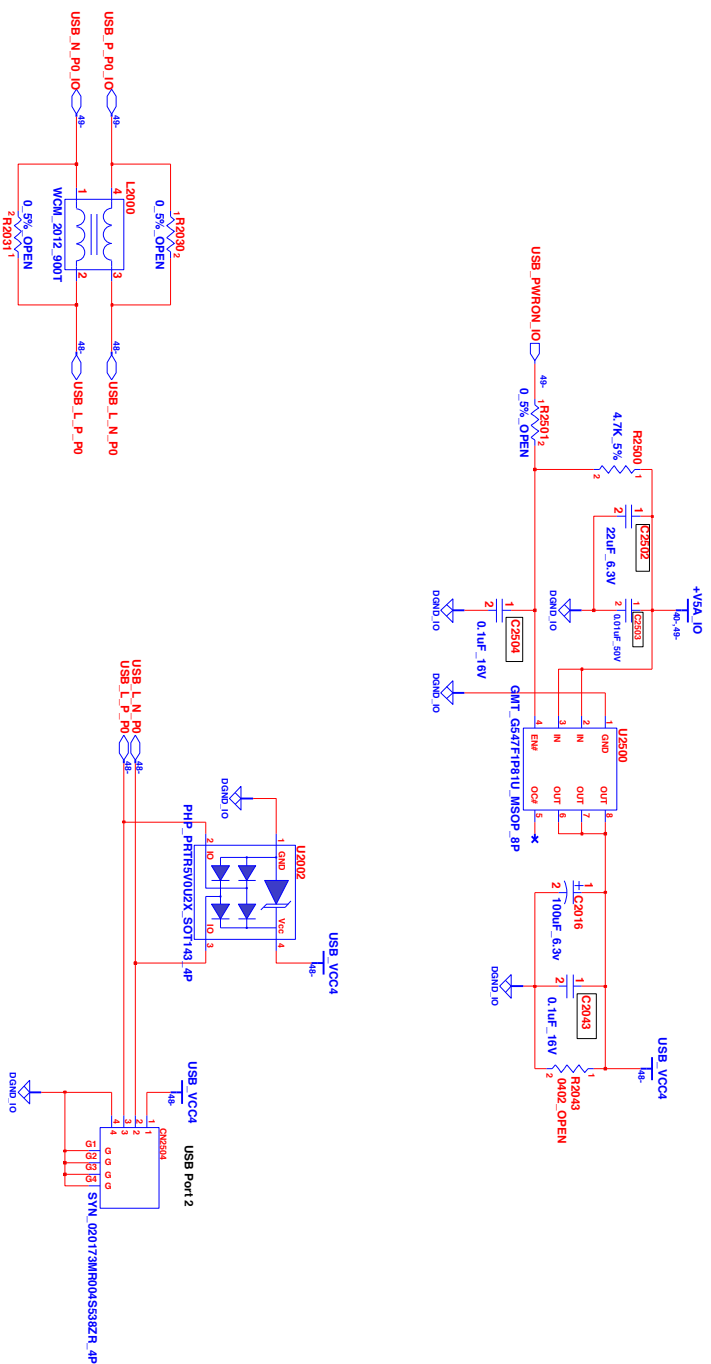
REV

A3 CS

SHEET

47 OF 54

USB CONN



INVENTEC

MIRANDA

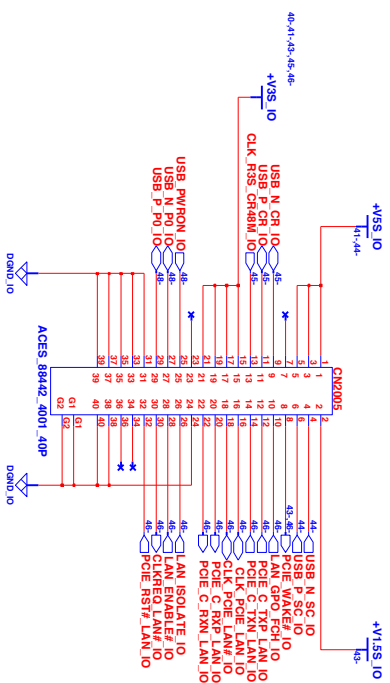
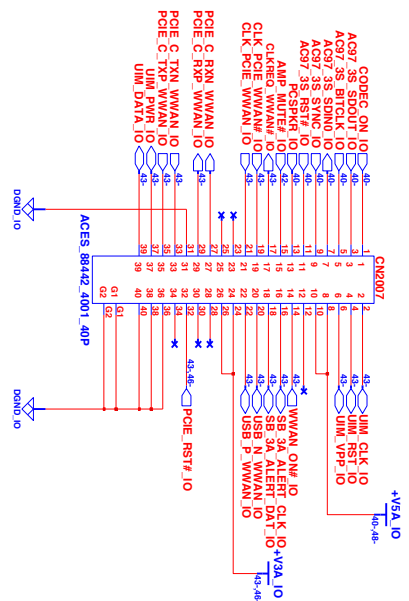
LITE Block Diagram

SIZE CODE DOC NUMBER REV
A3 CS 1310AXXX34-MIRANDA

SHEET 48 OF 54

CHANGED BY Drawn Name 7 10-Sep-2010

WIRE TO BOARD CONN (IO/B SIDE)



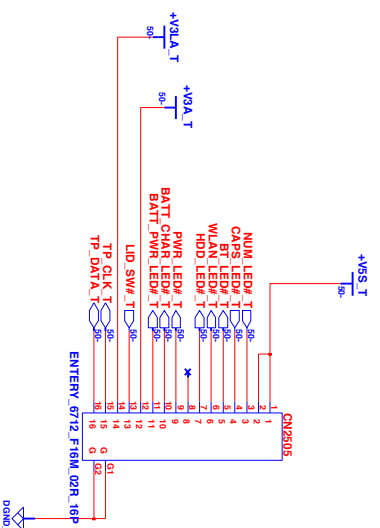
INVENTEC

MIRANDA

DIAGRAM

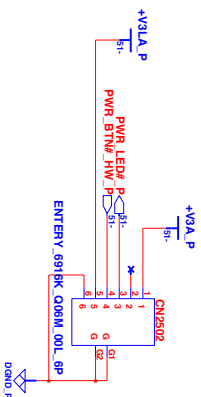
SIZE CODE DOC:NUMBER REV

A3 CS 49 07 54



WLAN_LED# & BT_LED#	PURPLE
WLAN_LED#	BLUE
BT_LED#	AMBER
NUM_LED#	BLUE
CAPS_LED#	BLUE
HDD_LED#	BLUE
PWR_LED#	BLUE
BATT_CHAR	AMBER
BATT_PWR	BLUE

<i>INVENTEC</i>			
TITLE			
<i>MIRANDA</i>			
<i>AUDIO JACK/ PWR SW</i>			
SIZE	CODE	DOC. NUMBER	REV
<i>A3</i>	<i>CS</i>		
SHEET		50	OF 54



SIZE	CODE	DOC. NUMBER	REV
A3	CS		

SHEET 51 OF 54

